

Remarks

I. Summary of the Examiner's Objections

In the Office Action mailed November 22, 2002, the Examiner subjected claims 1-31 and 59-76 of the application to a restriction requirement. On page 2 of the aforementioned Office Action, the Examiner grouped claims 1-31 and 60-74 as drawn to a circuit for quantifying a high voltage signal; claim 59 as drawn to a high voltage amplifying circuit utilizing a current differencing circuit; and claims 75-76 as drawn to a high voltage amplifying circuit utilizing a current steering circuit.

II. Election

Applicant hereby elects to proceed with the invention as defined in group 1 (claims 1-31 and 60 - 74 in the instant application.

III. Remarks

In view of the amendments to claims 75 and 76, it is respectfully submitted that such claims are now properly grouped with claims 1-31 and 59-76.

In view of the foregoing, Applicant respectfully asserts that the present application is in condition for examination. Should the Examiner have any questions with regard to the instant response, the Examiner is respectfully requested to contact the undersigned attorney.

Respectfully submitted,

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APPENDIX

Below are the marked-up copies of the amended claims.

75. (ONCE AMENDED) [A high-voltage amplifier comprising:
a first terminal receiving a low voltage;
a second terminal;
a first field transistor having a gate, a source, and a drain, the field transistor gate being coupled to the second terminal output;]

The circuit of claim 1 further including:

- a reference circuit;
a current-steering circuit having an input and an output coupled to said reference circuit.

76. (ONCE AMENDED) The [amplifier] circuit of claim 75 wherein the reference circuit includes at least a second field transistor.